

University of Mumbai

**1213261**

Winter 2025

EXAMINATION TIME TABLE (NEP-2020)

PROGRAMME - B.E. (Electronics Engineering (VLSI Design and Technology))

SEMESTER - III

Days and Dates	Time	Paper Code	Paper
Tuesday, 2 December, 2025	10:30 a.m. to 12:30 p.m.	2323111	Mathematics-III
Thursday, 4 December, 2025	10:30 a.m. to 12:30 p.m.	2323112	Electronic Devices
Monday, 8 December, 2025	10:30 a.m. to 12:30 p.m.	2323113	Digital System Design
Wednesday, 10 December, 2025	10:30 a.m. to 12:30 p.m.	2323114	Electrical Networks Analysis & Synthesis
Friday, 12 December, 2025	10:30 a.m. to 12:30 p.m.	OEC301	O/E (Open Elective)

Important Note: •The candidates appearing for the examination should report 20 minutes before the start of examination.

- Mobile phones and other electronic gadgets are prohibited in the examination hall.
- Change if any, in the timetable shall be communicated on the University website.

Mumbai - 400 098
27th October, 2025.

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Dr. Pooja Raundale
Director
Board of Examinations & Evaluation